

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated July 16, 2007, has been received and its contents carefully reviewed.

Claims 1-14 are rejected by the Examiner. With this response, claims 1, 7, and 13 have been amended. No new matter has been added. Claims 1-14 remain pending in this application.

Claims 1, 3, 4, and 6-14 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,535,194 to Hanano (hereinafter “Hanano”). Claims 2 and 5 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Hanano in view of U.S. Patent No. 4,097,128 to Matsumoto (hereinafter “Matsumoto”).

The rejection of claims 1, 3, 4, and 6-14 is respectfully traversed and reconsideration is requested.

Claims 1, 3, 4, and 6-12 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, “a light shutter on the liquid crystal display panel operable to transmit and shut off a light emitted from the liquid crystal display panel during every field period” and “wherein every field period corresponds to only one image data value.” Hanano does not teach or suggest at least this feature of the claimed invention.

In the Office Action the Examiner refers to Figs. 4c and 4f as teaching these features. But, Hanano does not teach these features of claims 1, 3, 4, and 6-12. The field synchronizing signal shown in Fig. 4f is only applied every other frame or field, and this is contrary to the present invention. The Examiner in the rejection states that “Even and odd halves make up a single frame image which is a single image data value.” See page 3 of the Office Action. The claim as amended recites “wherein every field period is initiated upon a first transition of a gate signal from a low voltage signal to a high voltage signal to apply image data to the pixel and is terminated upon a next transition of the gate signal from a low voltage signal to a high voltage signal to apply image data to the pixel.” Accordingly, Applicant submits the field period as recited in claim 1 and 7 does not include “Even and Odd halves . . . of a single frame image” as stated by the Examiner, and that that Hanano does not teach or suggest at least the above identified feature of claims 1, 3, 4, and 6-12. Applicant submits that claims 1, 3, 4, and 6-12 are allowable over Hanano for at least this reason.

Claims 13 and 14 are allowable over the cited references in that each of these claims recites a method of driving a liquid crystal display, having a combination of features including,

for example, “opening the light shutter at an initial interval upon application of a video data voltage to the pixel and closing the light shutter in a maintenance interval maintaining the video data voltage for the pixel to shut off a light from the liquid crystal display panel during every field, wherein every field corresponds to only one video data value.” In the Office Action, the Examiner rejects claim 13 using the same rationale given for claim 1. Applicant’s submits that Hanano does not teach or suggest at least the above identified features of claims 13 and 14 for at least the reasons given above for claim 1. Accordingly, Applicant submits that claims 13 and 14 are allowable over Hanano.

The rejection of claims 2 and 5 is respectfully traversed. As discussed above with respect to claims 1, 3, 4, and 6-14, Hanano fails to teach the features discussed above. Matsumoto fails to cure this deficiency of Hanano. Accordingly, Applicant submits that claims 2 and 5 are allowable over Hanano and Matsumoto.

Applicant believes the application is allowable in view of the foregoing amendments and remarks and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. *A duplicate copy of this sheet is enclosed.*

Respectfully submitted,

Dated: October 15, 2007

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